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| Examiner Initials*       | Cite No. <sup>1</sup> | Foreign Patent Document   |                     |                                      | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Translation <sup>6</sup> |
|                          |                       | Country Code <sup>3</sup> | Number <sup>4</sup> | Kind Code <sup>5</sup><br>(if known) |                                |  |                          |
|                          |                       | JP                        | 2002-289807         | A                                    | 18-11-2002                     |  |                          |
|                          |                       | JP                        | 2002-280520         | A                                    | 09-27-2002                     |  |                          |
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|                          |                       | JP                        | 2004-179192         | A                                    | 06-24-2004                     |  |                          |

## **NON PATENT LITERATURE DOCUMENTS**

| Examiner Initials* | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published. | Translation <sup>6</sup> |
|--------------------|-----------------------|--|--------------------------|
|                    |                       | Durlam M. et al. "A LOW POWER 1 MBIT MRAM BASED ON 1T1MTJ BIT CELL INTEGRATED WITH COPPER INTERCONNECTS," 2002 Symposium on VLSI Circuits Digest of Technical Papers, pages 158 to 161.  |                          |
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| Examiner Signature | /Alexander Sofocleous/ | Date Considered | 03/08/2008 |
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